

PA20-08-2PD(Z)3(6) Data Sheet

20 pin PLCC Auto-eject **Dead-Bug** socket/8 pin DIP 0.3" (0.6") plug
or

20 pin PLCC Lidded ZIF **Live-Bug** socket/8 pin DIP 0.3" (0.6") plug

Supported Device/Footprints

This adapter allows several Xilinx devices in the PLCC package to be programmed in their DIP footprint. Other devices that require this 20 to 8 pin wiring are also supported.

Adapter Part Numbers

The following chart identifies all the versions of this adapter currently available and the correct placement of the device to be programmed. For more information see the drawings at left.

Adapter	Plug	Socket Style	Device Placement
PA20-08-2PD3	0.3"	Auto-eject	Pins up
PA20-08-2PD6	0.6"	Auto-eject	Pins up
PA20-08-2PZ3	0.3"	Lidded ZIF	Pins down
PA20-08-2PZ6	0.6"	Lidded ZIF	Pins down

Adapter Construction

The adapter is made up of 3 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced when they wear out.

When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

Test Socket

PLCC Auto-Eject test socket:

Yamaichi Part #: IC120-0204-205 LSC Part #: 20-205

PLCC Lidded ZIF test socket:

Yamaichi Part #: IC51-0204-602 LSC Part #: 20-602

20PLD(Z)

Accepts the test socket and connects the signals to the bottom board.

20-08-2-3(-6)

Accepts the top board and re-maps the signals to a DIP footprint.

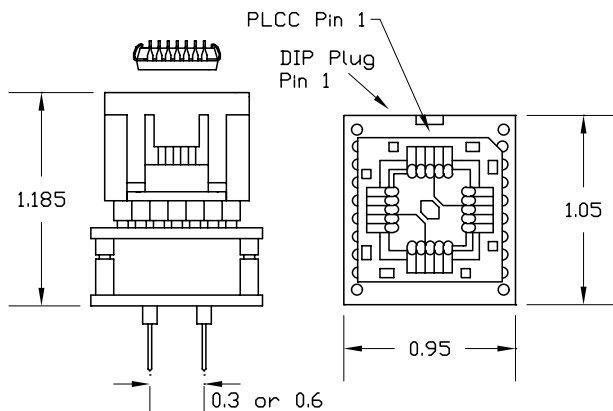
Adapter Wiring

The following chart shows the connections from the PLCC device to the adapter's DIP plug.

DEVICE	SIGNAL	PLUG	PLUG	SIGNAL	DEVICE
1	-	-	8	VCC	20
2	DATA	1	-	-	19
3	-	-	-	-	18
4	CLK	2	7	VPP	17
5	-	-	-	-	16
6	OE/RESET	3	-	-	15
7	-	-	6	CEO	14
8	CE	4	-	-	13
9	-	-	-	-	12
10	GND	5	-	-	11

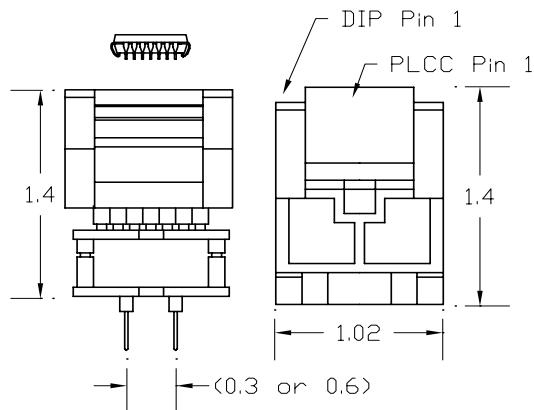
Adapter Dimensions

NOTE: THIS IS A DEAD-BUG SOCKET
THE PLCC DEVICE IS INSERTED UPSIDE-DOWN



PA20-08-2PD3(6)

NOTE: THIS IS A LIVE-BUG SOCKET
THE PLCC DEVICE IS INSERTED RIGHT-SIDE-UP



PA20-08-2PZ3(6)

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