PA756-68 Data Sheet

68 pin PLCC socket/40 pin DIP 0.6” plug

Supported Device/Footprints

This adapter allows device programming of the Microchip PIC17C75x-68PLCC in the Microchip 17C44-40DIP footprint.

Device			Footprint	
Mfgr	Device	Package	Device	Plug
Microchip	PIC17C752	68 PLCC	PIC17C44	40 Pin DIP
Microchip	PIC17C756	68 PLCC	PIC17C44 *	40 Pin DIP

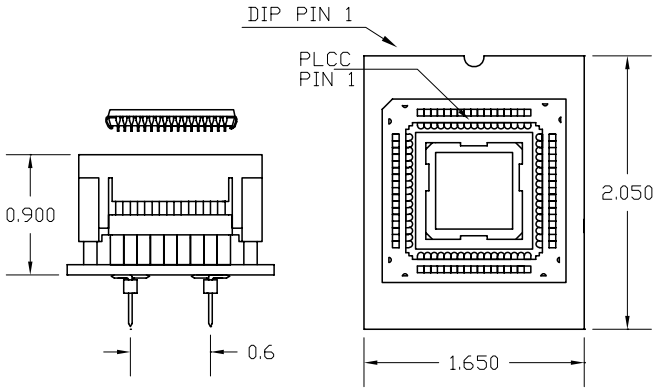
Compatibility Issues

Microchip designed the 17C75x devices to use the programming footprint and algorithm of the 17C44. The 17C752 has the same size EPROM and will work correctly.

The 17C756 has 16K EPROM and the 17C44 has 8K EPROM. The programmer must control the extra address line to properly program the 17C756.

A menu selection for the 17C75x may be provided by your programmer. It will probably work with this adapter.

Adapter Dimensions



Press rim to open socket, press device to close

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Adapter Construction

The adapter is made up of 2 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced when they wear out.

When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

Test Socket
PLCC Auto-Eject test socket:
Yamaichi Part #: IC120-0684-304 LSC Part #: 68-304

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Accepts the test socket and performs the wiring shown in the Adapter Wiring section.

Adapter Wiring

The following chart shows the connections from the PLCC device to the adapter’s DIP plug.

PLCC	SIGNAL	DIP	DIP	SIGNAL	PLCC
1	N/C	-	31	VSS	68
2	VDD	1	3	AD1	67
3	AD0	2	4	AD2	66
4	AD15	33	5	AD3	65
5	AD14	34	6	AD4	64
6	AD13	35	7	AD5	63
7	AD12	36	8	AD6	62
8	AD11	37	9	AD7	61
9	AD10	38	26	INT	60
10	AD9	39	11	CAP1	59
11	AD8	40	12	CAP2	58
12	ALE	30	14	PWM2	57
13	OE*	29	15	TCLK12	56
14	WR*	28	16	TCLK3	55
15	N/C	-	13	PWM1	54
16	VPP	32	31	VSS[1]	53
17	TEST	27	-	N/C	52
18	N/C	-	20	OSC2	51
19	VSS[1]	10	19	OSC1	50
20	VDD[2]	1	1	VDD[2]	49
21	N/C	-	18	RB7	48
22	N/C	-	17	RB6	47
23	N/C	-	23	RA3	46
24	N/C	-	24	RA2	45
25	N/C	-	25	RA1	44
26	N/C	-	22	Rx	43
27	N/C	-	21	Tx	42
28	N/C	-	-	N/C	41
29	N/C[3]	-	-	N/C	40
30	N/C[3]	-	-	N/C	39
31	N/C	-	-	N/C	38
32	N/C	-	1	VDD[2]	37
33	N/C	-	31	VSS[1]	36
34	N/C	-	-	N/C	35

1: VSS: DIP 10 & 31; PLCC 19, 36, 53, 68
2: VDD: DIP 1; PLCC 2, 20, 37, 49
3: Not connected on adapter Ver B. On Ver A Pin 29 was VSS and Pin 30 wasVDD.

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